MC8051 IP Core - Feedback Form

Please complete the form and return it either per email to **contact@oregano.at** or fax it to: **+43 1 253 30 33-6162**

Your name, your email address and your company/organization:

(blank box)

We may add your comments to our web site/documents: ☐ yes   ☐ no

	☺		😐		☹
Overall quality of the MC8051 IP core	☐	☐	☐	☐	☐
Easy to implement the MC8051 IP core on FPGA/ASIC	☐	☐	☐	☐	☐
Easy to embed the MC8051 IP core in design flow	☐	☐	☐	☐	☐
Easy to parametrize the MC8051 IP core	☐	☐	☐	☐	☐
Easy to simulate the MC8051 IP core	☐	☐	☐	☐	☐
Easy to adapt/edit/change the MC8051 IP core	☐	☐	☐	☐	☐
Documentation	☐	☐	☐	☐	☐

On which technology did you implement the 8051 IP core?
e.g. Altera APEX20kE200 or LSI 0,18µm standard cell

(blank box)

Which volume are you expecting for your product?

(blank box)

Which features are you missing?

(blank box)

Any further comments?

(blank box)