syn1588 ®

syn1588® Linux Live System

Release Notes

Version 1.10 – October 30th 2018

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0.1 Legals

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0.2 Contents

1 Release 2018-10-29

1.1 Change log syn1588® PTP Stack (V 1.7-14 Rev gc10fa03)

- 86eb2f5a9d: Added support for G8275_1 and G8275_2 profiles for Windows

- 850c48664e: Implement layer 2 mode for Windows

- 5253479dae: Fix lucky packet filter

- a37846e04c: Fix offset compensation in P2P mode

- abc785453c: Fix parser error message when domain is out of range

- 4ce4b83a9b: Fix segmentation fault in layer 2 mode for Linux

- d6d39e21d: Fix BMCA failure

- 5d836ada3e: Update parser testcase reference

- F21aa64c59: Add support to handle CTRL-BREAK signal for Windows

- 3ec4db0d04: Fix bug in Management Handler

- bb8d79e6b: Finished implementation of profile C37.238-2017

- 887573b90e: Remove internal management port

1.2 Change log for ptpmmm (V 1.7-14 Rev gc10fa03)

- 3ec4db0d04: Fix bug in Management Handler

1.3 Change log for lSync (V 1.7-14 Rev gc10fa03)

- Dbfbaca954: Fix issue when handing event inputs

1.4 Change log for eSync (V 1.7-14 Rev gc10fa03)

- Dbfbaca954: Fix issue when handing event inputs

1.5 Change log for fSync (V 1.7-14 Rev gc10fa03)

- No changes

1.6 Change log for vSync (V 1.7-14 Rev gc10fa03)

- No changes

1.7 Change log for syn1588 app (V 1.7-14 Rev gc10fa03)

- No changes

1.8 Change log for syn1588® PTPMMM GUI (v1.6-206-ga026da8)

- a026da86a8: Fix parsing of time properties dataset

- 37b304bb11: Fix issue: PTPMMM GUI stops working after midnight

1.9 Change log syn1588® PCIe NIC Linux driver (1.7-0-g1293164)

- 1e5ad520c4: add "blacklist_altera_cvp.sh" to blacklist the altera_cvp kernel module

- 391d58695d: Update driver to support kernel version 4.15+

1.10 Known Issues

1.10.1 Leap Seconds Regression:

During our current release tests, we detected a regression in our Leap Seconds handling code.

1.10.1.1 Affected products:

The issue only affects the syn1588® PTP-Stack when it is running in software mode.

If you are running our syn1588® PTP-Stack in combination with our syn1588® PCIe NIC or one of our syn1588® IP cores you are not affected by this regression.

1.10.1.2 Known effects of this regression:

The premonition phase of a leap second starts 24 hours before the leap second and ends with the leap second event itself.

If a syn1588® PTP-Stack is started in Slave mode during the premonition phase of an upcoming leap second, the syn1588® PTP-Stack will fail to properly respond to the leap second.

This will lead to a discontinuity in the offset of the syn1588® PTP-Stack of about one second at the time of the leap second.

The control loop of the syn1588® PTP-Stack will readjust itself after this event.

If the syn1588® PTP-Stack running in Slave mode was started before the premonition phase, the syn1588® PTP-Stack will properly respond to the leap second and there will be no discontinuity.

If you have any further questions related to this issue, please contact us at support@oreganosystems.at

2   Further Information

You are looking for further information about our syn1588® product line-up? Please contact Oregano Systems support! We will be pleased to provide you all the required information.

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